

Department of Computer Science & Engineering National Institute of Technology, Raipur

(An Autonomous Institute of National Importance, MoE, Govt. of India) G.E. Road, Raipur-492010 (C.G.)

Advertisement for the Post of Project Fellow (PF) in CCOST Funded Project

Applications are invited from qualified and highly motivated Indian nationals for the position of PF (Project Fellow) in a CCOST funded project with the following details:

Title of the Project: Automated elephant tracking & early alert system for minimizing human-animal conflicts in Chhattisgarh

Number of PF Positions: 01 (One)

Essential qualifications: B.E. /B. Tech. in any discipline **(OR)** B.Sc. in Computer Science /Information Technology (or equivalent) from a recognized institute/university. **(OR)** M.E. /M. Tech. /M.S./ MCA/M.Sc. in Computer Science and Engineering /Information Technology Engineering/Artificial Intelligence (or equivalent) (with specialization relevant to the title of the project) from a recognized institute/university.

Desirable: Preference will be given to the candidates having experience relevant to the project such as working experience on Node JS, Angular JS, HTML, JavaScript, Database (MySQL, Mongo DB), Payment gateway Integration, Bootstrap 5, CSS3, Programming languages (Python/Java, Android), Machine learning, Deep learning, etc.

Age limit: Preferably below 40 years.

Salary: Rs. 10,000/Per Month (Fixed)

Duration: The initial appointment will be offered for six months and is extendable based on performance review for the next six months or till the completion of the project (Maximum project duration 24 Months).

How to apply: Interested candidates must apply on the application form attached to this advertisement. The duly completed application form along with the candidate's resume must be mailed or emailed to the PI so as to reach him by 25/10/2024. Only shortlisted candidates will be called for interview. The positions are available immediately.

Due date: The application must reach the PI, Dr. Dilip Singh Sisodia (dssisodia.cs@nitrr.ac.in), by 25/10/2024.

General Terms and Conditions:

- 1. Only Indian nationals are eligible to appear for the written test and/or interview.
- 2. The position is purely temporary and renewable for six months subject to satisfactory performance for a maximum of 24 months or till the completion of the project whichever is earlier.
- 3. Only shortlisted candidates will be communicated to appear in the written test and/or interview and no other communication in this regard will be entertained.
- 4. Original documents of age proof/certificates/degrees/mark sheets must be presented at the time of the written test and/or interview.
- 5. The candidates have to attend the selection process in person.
- 6. No TA/DA will be paid for attending the written test and/or interview.

Selection Procedure:

PF will be selected by conducting a presentation/Interview. However, a written test will be conducted if the number of candidates increases.

How to Apply:

A Google form (https://forms.gle/moLbG2uNTYH5fMD48) should be filled in all aspects with the relevant academic and professional details.

- The filled application should be brought with self attested copies of Date of birth Certificate, Degree/Provisional Certificate along with Marks Memos, GATE score card, Caste Certificate, No Objection Certificate, other testimonials and also Original Certificates for verification.
- The candidate must submit the supporting document for converting CGPA / CPI to equivalent percentage (%) from concerned University. If not submitted, the candidate is liable to be rejected.

Submission Deadline for Application Form: November 16, 2024

Tentative Date of Presentation and Interview: November 16, 2024, at 11:00 AM

For further details or any query, you may contact: Dr. Dilip Singh Sisodia (PI)/ Dr. Deepak Singh (Co-PI), Department of Computer Science & Engineering, National Institute of Technology Raipur, Email: dssisodia.cs@nitrr.ac.in/dsingh.cs@nitrr.ac.in Contact Number: +91-8462808174/98279167078.

Project PI-Dr. Dilip Singh Sisodia, Associate Professor Department of Computer Science & Engineering National Institute of Technology, Raipur, Chhattisgarh, 492010.